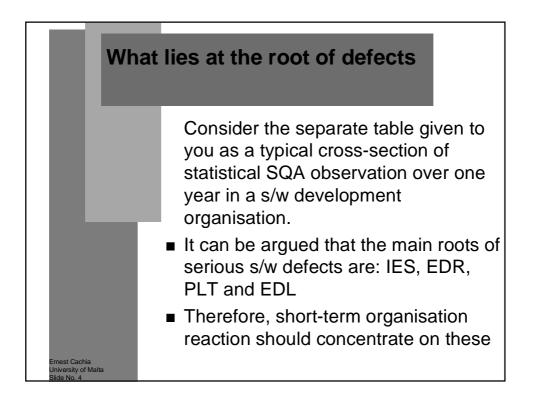
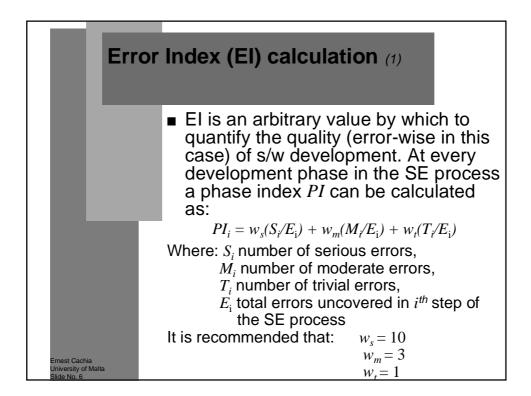
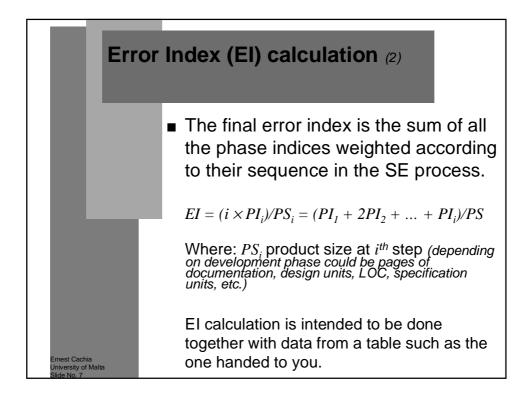


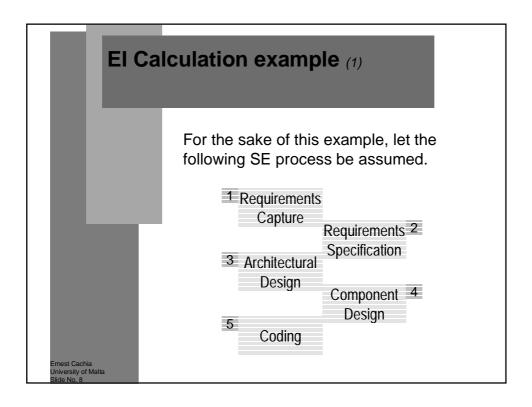
| | The Causes of Software Defects |
|------------------------------------|---|
| Ernest Cachia University of Mal | Incomplete or erroneous spec. (IES) Misinterpretation of customer comm. (MCC) Intentional deviation from spec. (IDS) Violation of programming standards (VPS) Error in data representation (EDR) Inconsistent module interface (IMI) Error in design logic (EDL) Incomplete or erroneous testing (IET) Incomplete or inaccurate documentation (IID) Programming language translation of design error (PLT) Ambiguous or inconsistent HCI (HCI) Miscellaneous (MIS) |

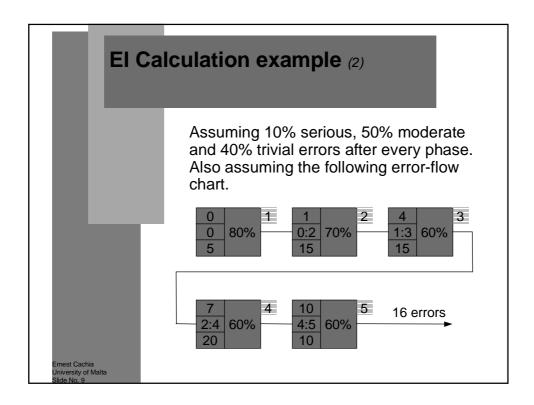


| Corrective measure examples | | |
|---|--|--|
| Ernest Cachia University of Malta Slide No. 5 | IES - Improve specification techniques, introduce new methods, upgrade personnel, etc. EDR - Adopt automated data design tools, impose stringent data modelling and reviews, etc. PLT - use more visibility, check design phase output, enforce strict translation techniques, etc. EDL - reinforce good requirements understanding, ensure personnel quality, adopt widespread design techniques, etc. | |









| EI | Calculation example (3) |
|------------------------------------|--|
| Emest Cachia University of Mata | Error breakdown after each phase: 1: Requirements capture 1 error: 0 serious / 1 moderate / 0 trivial 2: Requirements specification 5 errors: 0 serious / 3 moderate / 2 trivial 3: Architectural design 9 errors: 0 serious / 5 moderate / 4 trivial 4: Component design 14 errors: 1 serious / 7 moderate / 6 trivial 5: Coding 16 errors: 2 serious / 8 moderate / 6 trivial |

